application Sttorney Docket SE THE UNITED STATES PATENT AND TRADEMARK OFFICE In Re Application of "Express Mail" Mailing Label No. EL 613564191US Hiroki ADACHI Date of Deposit May 9, 2002 Serial No.: 09/441,026 I hereby certify that this correspondence is being deposited with the United States Postal Service Filed: November 16, 1999 "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above Exposure Device, and is aldressed to: Commissioner For: Exposure Method And Method) for Pai ints, Washington, D.C. 20231 of Manufacturing Semiconductor Device Art Unit: 2814

Commissioner for Patents Washington, D.C. 20231

Examiner: S. Rao

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PROOF OF PRIOR SUBMISSION

Sir:

On May 9, 2002, the Examiner assigned to this case called the undersigned regarding a response to the Restriction Requirement of October 1, 2001. In particular, the Examiner stated that he had not received a response to this Restriction Requirement. The undersigned checked his file and confirmed that an election was sent to the PTO on November 1, 2001 which is within the required time period. A copy of that election is enclosed herewith. This election had a certificate of mailing signed November 1, 2001. The undersigned has received a stamped post card from the PTO showing that the PTO did receive this election. A copy of the stamped post card is enclosed.

Therefore, Applicant has fully responding to the outstanding restriction requirement and requests examination of this application.

Please charge Deposit Account No. 50-1039 for any fee due for this submission.

t and NECE ACD

Favorable consideration is earnestly solicited.

Respectfully submitted,

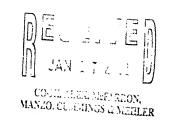
Date: May 9, 2002

Mark J. Murphy

Registration No. 34,225

COOK, ALEX, McFARRON, MANZO, CUMMINGS & MEHLER, LTD. 200 West Adams Street Suite 2850 Chicago, Illinois 60606 (312) 236-8500





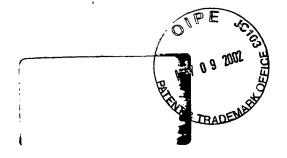
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ENCLOSURES:
() Application
RE: APPLICATION ATTY/SEC: MJM Plan File no: SEL 144 Applicant: Hiroki Adachi
S.N.: 09/441,026 Filing Date: November 14, 1990 Title: Exposure Device, Exposure
Method and Method of Manufacturing Semiconour or Device Manufacturing

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